

Ben A. Laws, Jr.

CURRENT ADDRESS:

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EDUCATION:

Ph.D. in Electrical Engineering
Montana State University
Thesis topic: Error Correcting Codes, a VLSI implementation.
Thesis title: "A Parallel BCH Decoder."

B.S. in Electrical Engineering
Montana State University

PROFESSIONAL EXPERIENCE:

System and language experience.

Languages: C, C++, Perl, Shell, Fortran, L-STAR, APL, expect, Object Oriented (C++, Logo, Smalltalk); Operating systems: UNIX v7, System V, SunOS, Solaris 10, SCO, Linux, pSOS, VRTX, MAC OSX, Windows 3.1, 95, 98, NT, 2000, XP, Server 2008R2, 2012; Databases: Informix, Oracle, Postgresql, MSSQL; Processors: Sparc, 80*86, x64, 68000, Z80, Z8000. Forensic tools: ENCASE computer forensic analysis. Computer Security: SANS Security training and currently certified by GIAC, (Global Information Assurance Certification): GCUX Gold, Certified UNIX security Administrator, GCFW Gold, GICA Certified Firewall Analyst Firewall and VPN.

President, Ben Laws and Associates, August 1984 to Present.

Consolidated existing Solaris servers using Solaris Zones. Virtualized various Solaris x86 stand-alone servers into VMWare virtual machines. Created a VMWare ESXi 5 lab trial installation leading to the existing data center for development and QA testing now serving several hundred Virtual machines. Assisted in specifying and implementing SAN access using the Dell Equallogic. Implemented iSCSI access for Legacy Solaris servers.

Porting Solaris legacy application to RedHat Linux 64-bit OS.

Porting Solaris legacy application to Win2k8 server OS.

Providing Sun Solaris 8-10 performance analysis for a legacy application. Security analysis, hardening, forensic analysis. Solaris 10 dtrace and Sun Cluster experience. Providing architecture and design recommendations to improve application performance and maintainability.

Providing support and problem resolution for multi-vendor OS, network and applications in live production environments including Veritas and Sun HA.

Provided expert consulting to Mr. William Cramer of Dykema Gossett PLLC for successful resolution of Trade Secret and Copyright infringement.

Provided expert consulting to Mr. Joe Gregory of Fee, Smith, Sharp & Vitullo LLP for patent infringement litigation on digital camera electronics systems.

Provided expert analysis and testimony for Mr. Peter Vogel of Gardere Wynne Sewell LLP regarding a legacy Windows 95 application regarding year 2000 (Y2K) issues. Performed Y2K expert analysis using PALM computing products.

Participated in forensic analysis and file recovery as an expert for Mr. Peter Vogel of Gardere Wynne Sewell LLP in litigation using the ENCASE package from Guidance Software.

Built and installed the first Web site for Gardere Wynne Sewell LLP using a Sun Solaris 8 web server; established the ISDN line access to the ISP, setup remote access and secured the server.

Provided technical support and consultation to the State Bar of Texas and Mr. Peter Vogel of Gardere Wynne Sewell LLP for a Practicum seminar for over 1200 legal professionals. Provide ongoing technical support for a local law firm and the Dallas Bar Association.

Designed and presented UNIX training seminars for engineering staff of Intecom, Inc. for their Sun workstations to introduce their designers to the Unix tools and methodology.

Responsible for the system architecture, team technical coordination and design of the QL-3000 Public Access Data Terminal system for Gammon Products. The hardware, mechanical, packaging and graphical software system was taken to a functional prototype stage after only 10 months.

Designed and implemented the protocol machine and user interface for the Agile Systems AN20 Local Area Network products, approximately 20,000 lines of C and assembler, cross compiler from UNIX, using VRTX Realtime OS.

Participated in the design and development of the Flex/32 Multi-Computer, the Concurrent C programming language and Multiprocessor UNIX for the Flex/32 Multi-Computer.

Contributed to the design and implementation of the Lantech Distributed File System for Personal Computers and PC systems.

IEX Corporation, principal account, 1989 to Present, additional detail.

Assist development, QA, performance, scalability groups with technical assistance and analysis on production servers. Did the initial feasibility work porting an existing Solaris based application (TotalView 4 / WFM) into both the Linux OS and the Windows Server OS. Performed network analysis and troubleshooting using Wireshark for TCP/IP, UDP and multicast issues on production servers to resolve performance and connectivity issues. Adapted an existing system product into the Sun Solaris 10 OS, including writing device drivers, automating patches and installation scripts. Designed and implemented locking, controls and procedures

for applications to use SUN DiskSuite mirroring to perform "online" backups. Implemented a SCHEMA based browser/debugger for a custom database.

Assistant Professor, Computer Science, North Texas State University, 1982–1984.

Responsible for bringing Berkeley UNIX to UNT, on VAX 11/780. Responsible for two courses and numerous seminars for NTSU staff and students on the UNIX operating system and the C programming language. Prepared and conducted UNIX Training Seminars for AT&T (Southwestern Bell), United Technologies, General Dynamics, Micro–America System Architecture consultant for Honeywell Communications Products Operation for next generation switching systems. Designed and implemented new UNIX device drivers for Unixsys in Paris, France.

Consultant to Italtel, Milan, Italy, 1981–1982.

Responsible for UNIX systems enhancements and user training in a software development environment for a large central office switch. Designed and implemented communications for developers using multiple UNIX systems in Dallas, Texas and Milan, Italy. System Architecture consultant for a large central office switch.

Vice President of Technical Development, 1979–1981, Advanced Business Communications, Inc., Dallas, Texas.

Responsible for new product invention, architecture and specification. Managed technical development of hardware and software for a UNIX based distributed processor digital central office switch. Recruited and managed a highly effective software development team of ten software engineers. Responsible for specifying, operating and maintaining two PDP–11/70 development systems running UNIX (Interactive Systems version 6 PWB).

Engineering Group Manager, 1978–1979, Zilog, Systems R & D, Cupertino, California.

Responsible for the system level design for a multi–processor Z8000 system. Coordinated the hardware and software architecture for the design of a multi–user operating system. Co–designer of a prototype coaxial cable network later called ZNET.

Senior Design Engineer, 1976–1978, Varian Associates, Palo Alto, California.

Designed and implemented interactive character graphics display hardware and software for a light–pen controlled man–machine interface to a large vacuum sputtering metal deposition system. Designed and implemented hardware and software for a multi–processor communications network in the process control environment. Implemented highly flexible software for the graphics display controller using Allen Newel’s experimental interpretive language ported to an 8086 controller (“L–STAR: An interactive, Symbolic Implementation System”, Newell, et al, 1977, NTIS Product No. ADA050119).

Member of the Research Staff, 1970–1976, Xerox Palo Alto Research Center, Palo Alto, California.

System designer for a large office automation project including specification and operation of a large multi–processor system. Contract manager for the first DanRay CBX to be used in the office automation project. Designed and

implemented hardware, software and microcode for a gray-scale and color graphics processor for 1024 line high-resolution CRT. Designed and implemented hardware and software for an integrated text and graphics editing system with video capture.

PROFESSIONAL AND HONORARY ORGANIZATIONS:

I.E.E.E. and I.E.E.E. Computer Society.

Association for Computing Machinery.

Past memberships: Tau Beta Pi, Phi Kappa Phi, Pi Mu Epsilon, Usenix.

OTHER ACTIVITIES:

Singing and Guitar playing, composing.

Active in Ham Radio—Amateur Extra Class License.

Hiking, Canoeing and camping.

PUBLICATIONS:

- [1] Laws, B. A. Jr., "Enable and Disable: Aids to Managing UNIX Terminals," Communixations, September–October, 1986.
- [2] Laws, B. A. Jr., Rubin, P. J., "MCPOS – A Realtime Telephone OS," 1986 IEEE conference on Computers and Communications, Phoenix, Arizona.
- [3] Laws, B. A. Jr., "Microbe: a self-commenting microassembler," Tenth annual IEEE conference on Microprogramming, Niagra Falls, New York, 1977.
- [4] Laws, B. A. Jr., "A Gray-Scale Graphics Processor using run-length encoding," Proceedings of the UCLA Conference on Computer Graphics, Pattern Recognition and Data Structure, pp. 7–10, 1975.
- [5] Laws, B. A. Jr., "A ROM Decoder for the (15,13) Reed–Solomon Code," IEEE Computer Repository, No. R-72–114, 1972.
- [6] Laws, B. A. Jr. and Rushforth, C. K., "A Cellular-array multiplier for GF(2^m)," IEEE Transactions on Computers, vol. C20, no. 12, pp. 1573–1578, December, 1971.
- [7] Rushforth, C.K., and Laws, B. A. Jr., "Fault testing and parity checking in a cellular array multiplier for GF(2^m)," Proceedings of the Fourth Hawaii International Conference on System Sciences, 1971.
- [8] Rushforth, C. K. and Laws, B. A. Jr., "Parallel Addition and Multiplication in GF(2^m)," Proceedings of the Third Hawaii International Conference on System Sciences, 1970.
- [9] Laws, B. A. Jr., "A Parallel BCH Decoder," Ph.D. Thesis, Montana State University, 1970.